	Туре	L#	Hits	Search Text	DBs
1	BRS	L1	9957	(impedance near2 match\$4) and (buffer driver invert\$r)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
2	BRS	L2	8376	1 and (input and output)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
3	BRS	L3	4778	2 and (transistor nmos\$4 pmos\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
4	BRS	L4	4450	3 and control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
5	BRS	L5	48	4 and (on\$2chip adj terminat\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

	Time Stamp	Comments	Error Definition	Er ror s
1	2004/10/26 15:44			0
2	2004/10/26 15:44			0
3	2004/10/26 15:45	· .		0
4	2004/10/26 15:45			0
5	2004/10/26 15:47			0